FUJI Discrete IGBT

Power vs. Tj, Tc, Tcs, Ts(chip), Ts

There is a temperature gap between T_{cs} which is measured on the topside of the case, and T_c which is measured on the bottom side under the chip. As an example, the graph shows measured data for the FGW50N65WD as reference. According to the measurement results, T_{cs} and $T_{s(chip)}$ which is measured on the heatsink under the chip are almost the same temperature. However, junction temperature T_j cannot be directly estimated from the T_{cs} . Please estimate T_j and T_c by $R_{th(c-s)}$ which is thermal resistance between case and heatsink after measuring $T_{s(chip)}$.

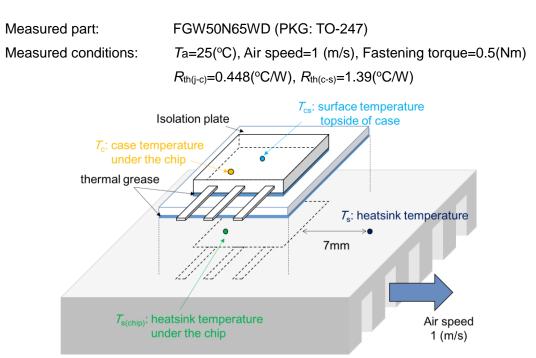


Fig. 1. Individual temperature measuring locations

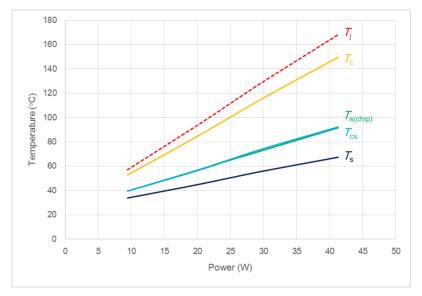


Fig. 2. Temperature as function of the power of the IGBT chip



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