
– Chapter 7 –

Troubleshooting

Table of Contents		Page
1	Troubleshooting	7-2
2	Failure analysis charts	7-2
3	Alarm factor tree analysis chart	7-8

1 Troubleshooting

An IPM has various integrated protective functions (such as overcurrent protection and overheat protection) unlike a standard module. It shuts down safely in the case of an abnormal condition. However, it may breakdown depending on the abnormality of the failure that occurred. When the IPM has failed, it is necessary to take countermeasures upon clarification of the situation and find the root cause of the breakdown.

Failure tree analysis charts are shown in Figure 7-1. Carry out the investigation of the failure mode by using these charts. For the failure criteria, see chapter 4, section 2 [IGBT test procedures] of the IGBT Module Application Manual (RH984b).

Furthermore, when an alarm signal is generated from the IPM investigation of the root cause by reference of the alarm factor analysis chart can be done as shown in Figure 7-2.

2 Failure analysis tree charts

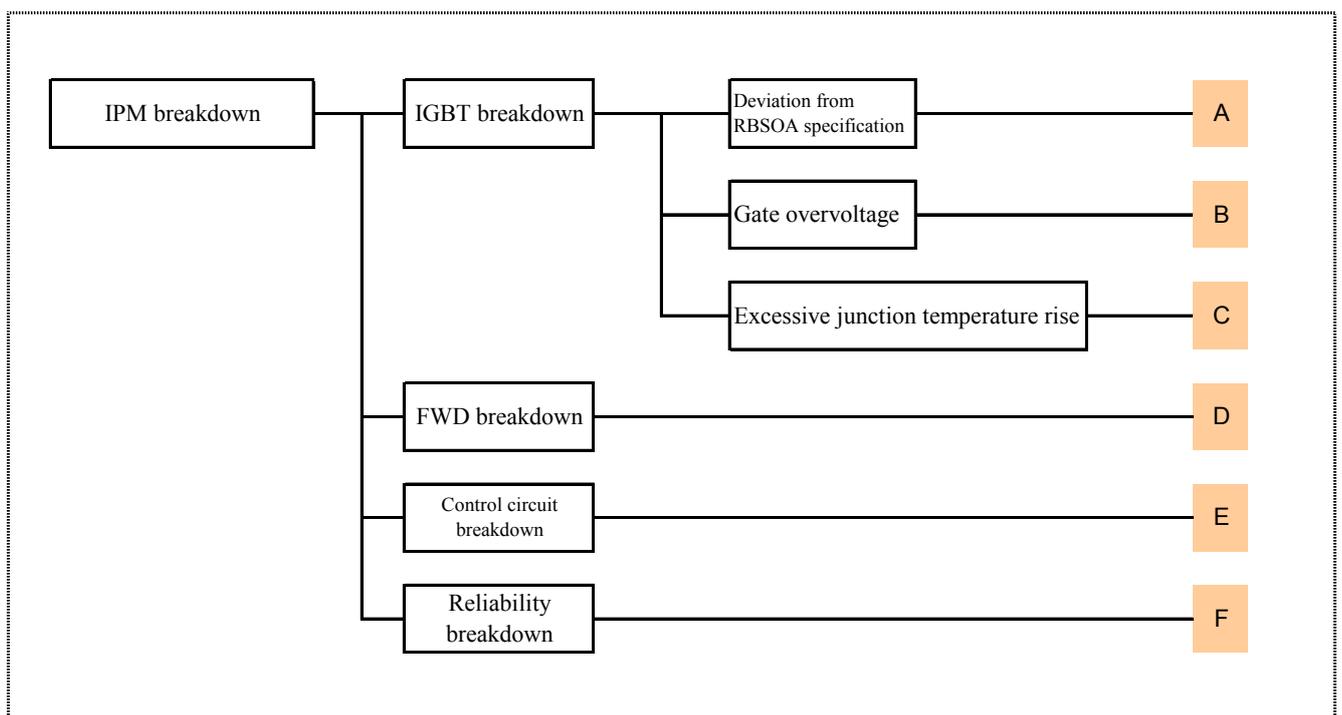


Figure 7-1 (a) IPM failure tree analysis chart

(Codes A to F are linked with those indicated in separate FTA pages.)

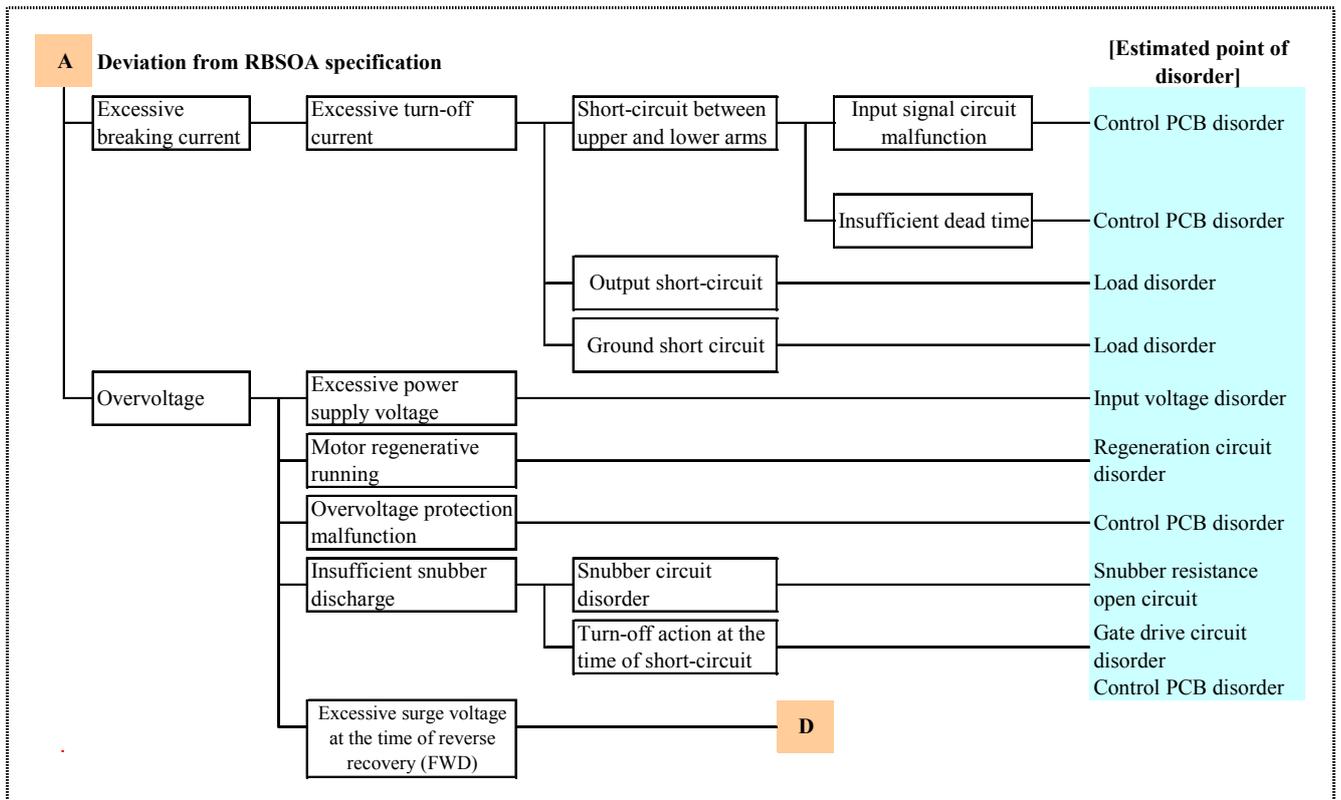


Figure 7-1 (b) Mode A: Deviation from RBSOA specification

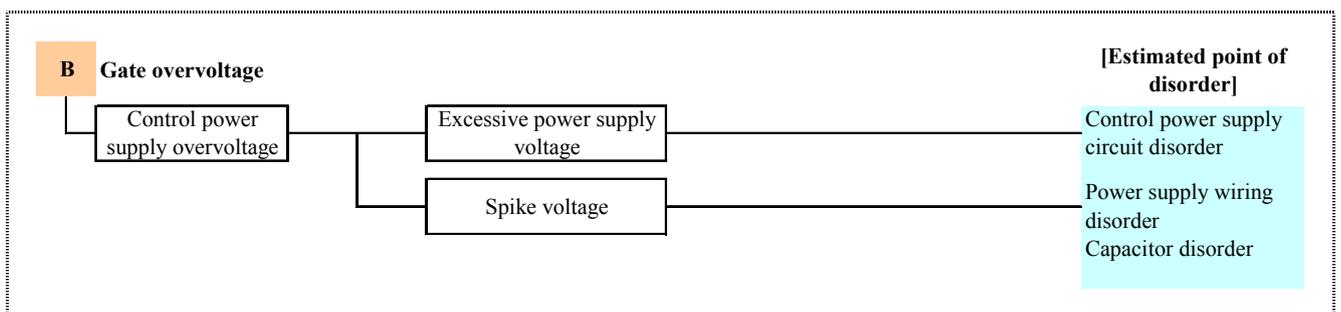


Figure 7-1 (c) Mode B: Gate overvoltage

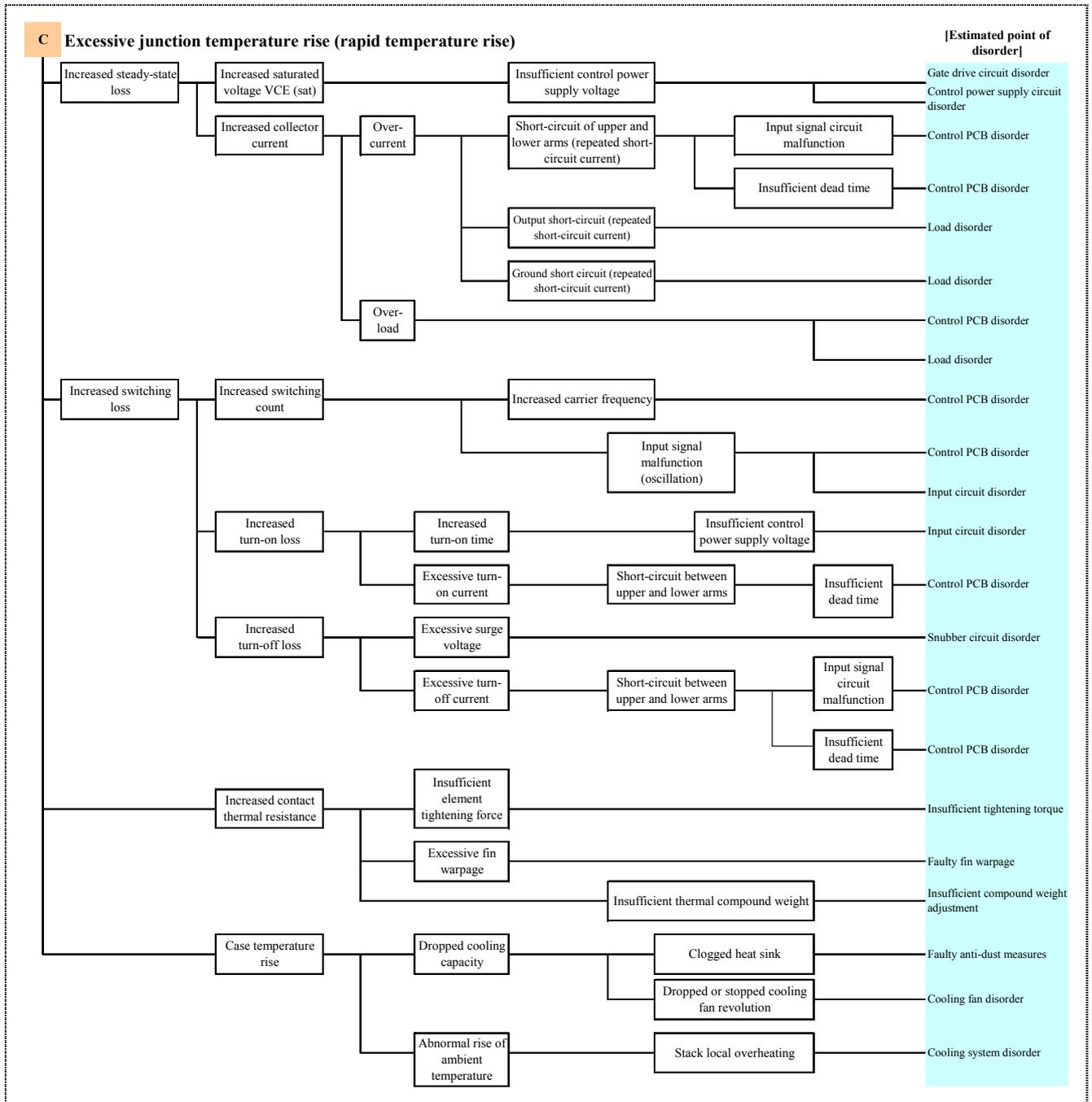


Figure 7-1 (d) Mode C: Excessive junction temperature rise

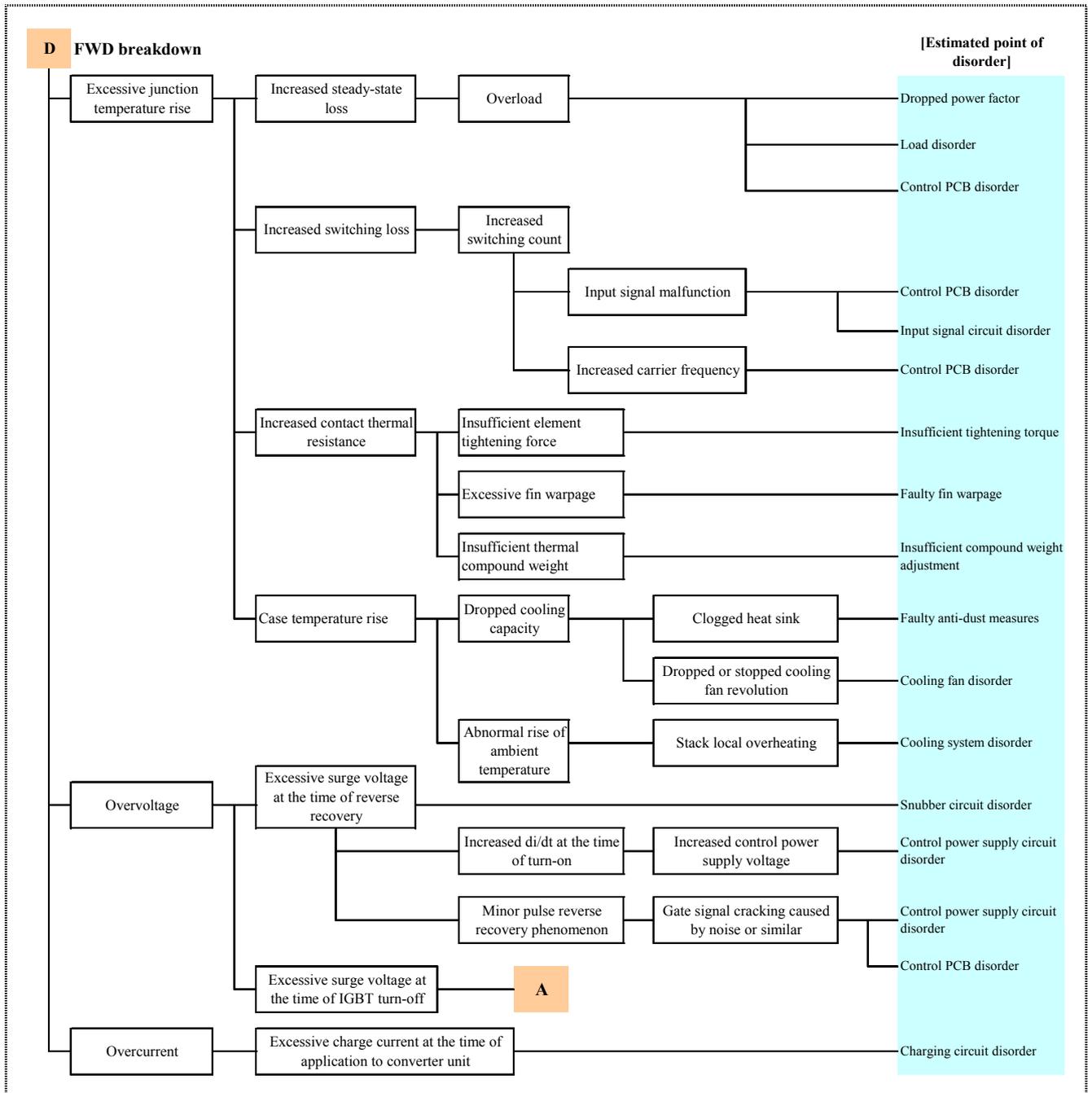


Figure 7-1 (e) Mode D: FWD breakdown

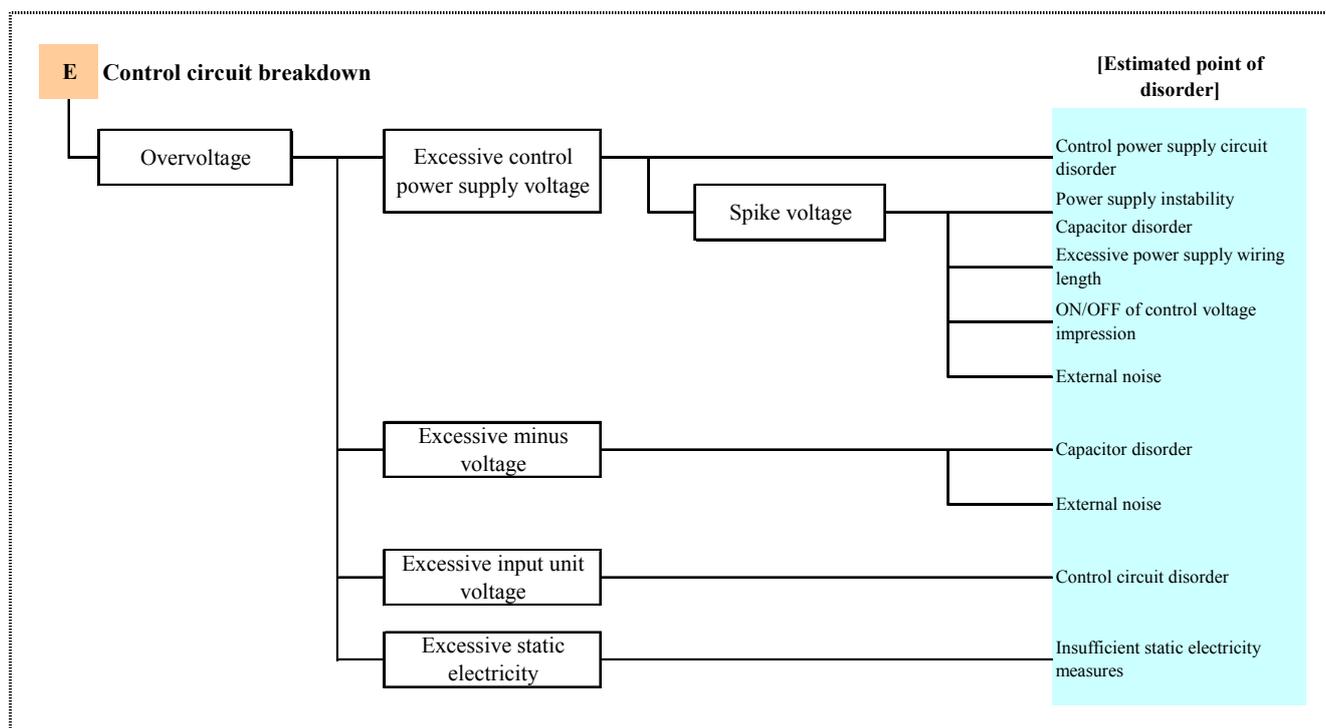


Figure 7-1 (f) Mode E: Control circuit breakdown

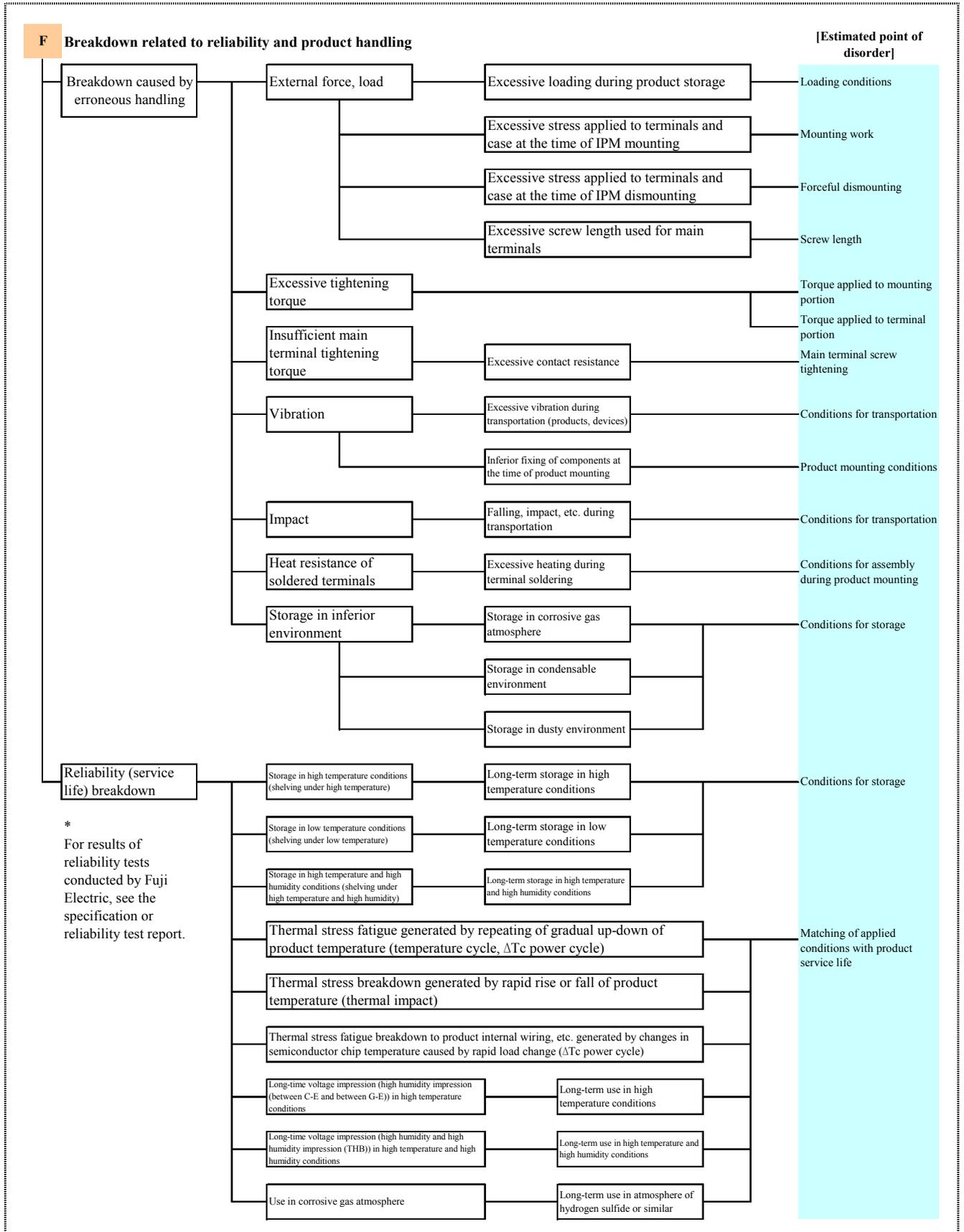


Figure 7-1 (g) Mode F: Breakdown related to reliability and product handling

3 Alarm factor analysis tree chart

When the system equipped with the IPM has stopped and an alarm signal is generated, first carry out investigations to identify where the alarm signal was generated from. Possible locations are at the IPM or the device control circuit.

If the alarm was sent from the IPM, then identify the factor in accordance with the factor tree chart indicated below. V-IPM is easy to identify which protective function is activated by checking the alarm pulse width. Therefore, you can shorten the factor analysis time.

In addition, the alarm output voltage can be easily measured by connecting a 1.3 K Ω resistor in series between the IPM alarm terminal and the cathode terminal of the alarming photodiode.

Phenomenon	Alarm factor and method for identification
<p>Occurrence of an IPM alarm</p> <ul style="list-style-type: none"> Normal alarm Overcurrent tALM Typ = 2 ms Low control power supply voltage tALM Typ = 4 ms Chip overheating tALM Typ = 8 ms Erroneous alarm Unstable tALM 	<p>The collector current is detected by checking the current that flows to the current sense IGBT that is built in every IGBT chip. The IGBT is OFF for protection, if the overcurrent trip level was continuously exceeded for about 5 μs.</p> <p>[Method for identification of alarm factor]</p> <ul style="list-style-type: none"> • Observe the alarm and output current (U, V, W) using an oscilloscope. • Observe the alarm and DC input current (P, N) using an oscilloscope. • Observe the change in the current 5μs before occurrence of alarm output. • Where a CT or similar is used for current detection, check the trip level and point of detection. <p>The IGBT is OFF for protection, if control power supply voltage Vcc was of undervoltage trip level or less continuously for 20 μs.</p> <p>[Method for identification of alarm factor]</p> <ul style="list-style-type: none"> • Observe the alarm and Vcc using an oscilloscope. • Observe the change in the current 20 μs before occurrence of alarm output. <p>The chip temperature is detected by the temperature detection element (diode) that is built in every IGBT chip. The IGBT is OFF for protection, if the TJOH trip level was exceeded continuously for 1 ms.</p> <p>[Method for identification of alarm factor]</p> <ul style="list-style-type: none"> • Measure control power supply voltage Vcc, DC input voltage Vdc and output current Io. • Measure case temperature Tc just below the chip, calculate ΔT_j-c and estimate the value of Tj. • Check the IPM mounting method. (Fin flatness, thermal compound, etc.) <p>If control power supply voltage Vcc exceeds absolute maximum rating, which is 20 V, or if excessive dv/dt or ripple was impressed, there is a possibility where the drive IC is broken and an erroneous alarm is output. Furthermore, also in case noise current flows to the IPM control circuit, there is a possibility where the IC voltage becomes unstable and an erroneous alarm is output.</p> <p>[Method for identification of alarm factor]</p> <ul style="list-style-type: none"> • A short-pulse alarm of an μs is produced. \Rightarrow See chapter 6 section 1.2.1 • Observe the Vcc waveform using an oscilloscope while the motor is running. It is desirable that the point of observation is located nearest to an IPM control terminal. • Confirm that Vcc < 20 V, dv/dt \leq 5 V/μs, ripple voltage \leq \pm10% (with every one of four power supply unit). • Confirm that no external wiring connection is made between IPM control GND and main terminal GND. If wiring is made, noise current flows through IPM control circuit. • If the drive IC was broken, there is a large possibility where the value Icc rises to an abnormal level. Example: It is abnormal, if Iccp \geq 10 mA, Iccn \geq 20 mA, and @Vin = OFF.